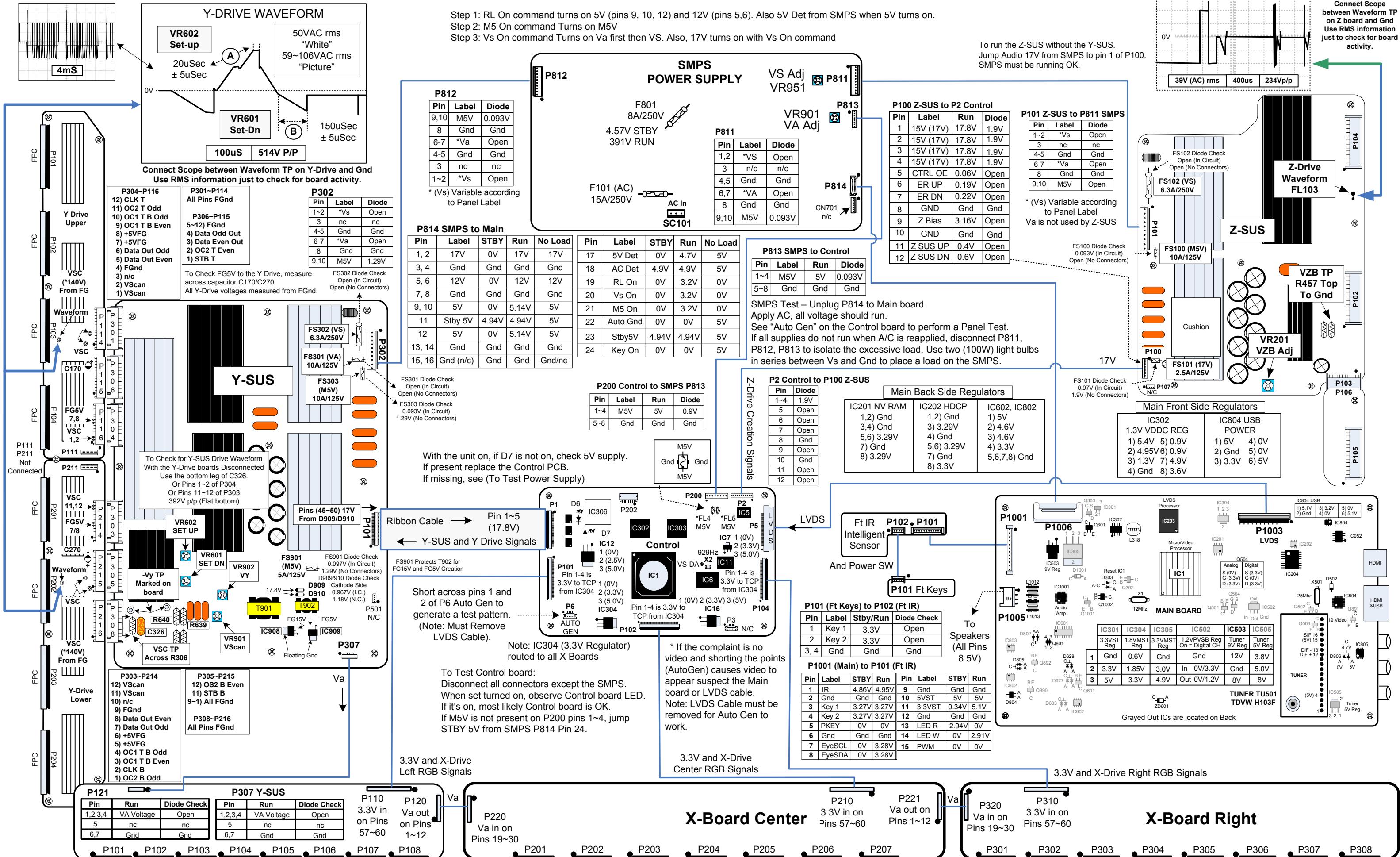
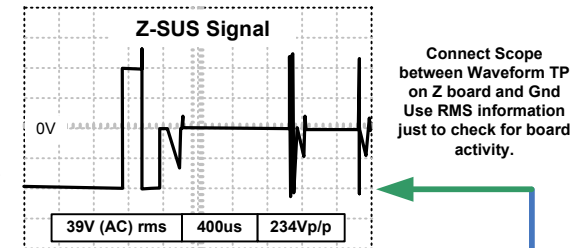


60PS60 (60H3 Panel) CIRCUIT INTERCONNECT DIAGRAM

NOTE: Diode tests are conducted with the board disconnected.

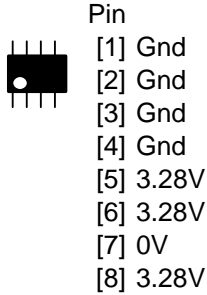
- Step 1: RL On command turns on 5V (pins 9, 10, 12) and 12V (pins 5,6). Also 5V Det from SMPS when 5V turns on.
Step 2: M5 On command Turns on M5V
Step 3: Vs On command Turns on Va first then VS. Also, 17V turns on with Vs On command

To run the Z-SUS without the Y-SUS.
Jump Audio 17V from SMPS to pin 1 of P100.
SMPS must be running OK.

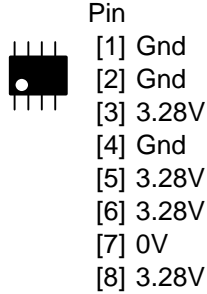


60PS60 MAIN (BACK SIDE) SIMICONDUCTORS

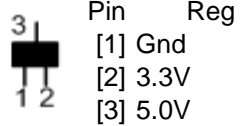
IC201 NVRAM



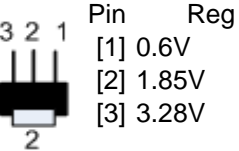
IC202 HDCP



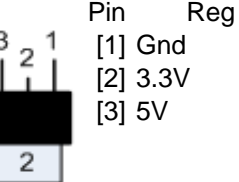
IC301 5VST to 3.3V VST



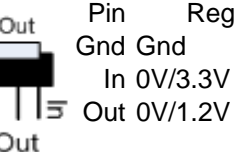
IC304 1.8VMST



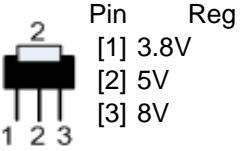
IC305 3.3VMST



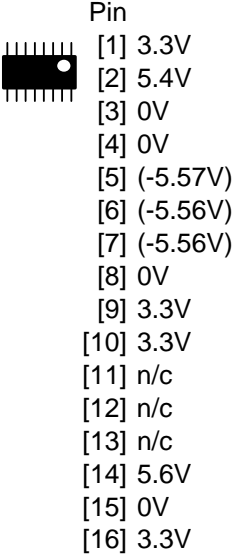
IC502 1.2V PVSB
On Digital CH / Off Analog



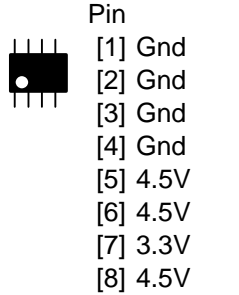
IC505 5V (Tuner)



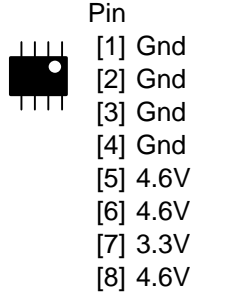
IC601 RS232 Control



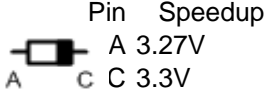
IC602 RS232 RAM



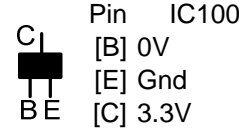
IC802/3 HDMI1/2



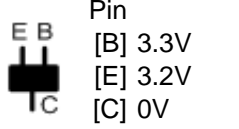
D1001 For IC1001 3.3V



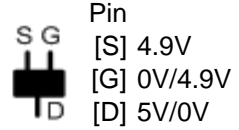
Q1001 Pow Down



Q302 Reset

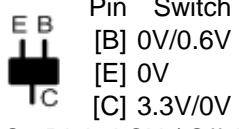


Q303 5V MST Switch



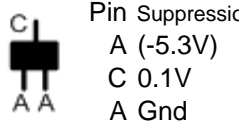
On Digital CH / Off Analog

Q501 3.3V VMST

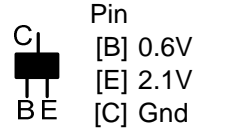


On Digital CH / Off Analog

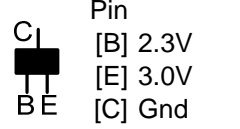
D627 RS232 TX Noise



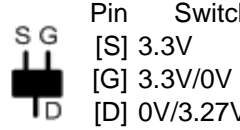
Q502 Video Buffer



Q503 SIF Buffer

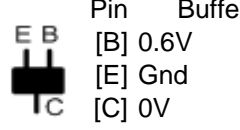


Q504 3.3V PVSB

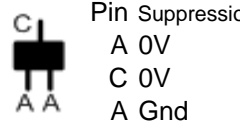


On Digital CH / Off Analog

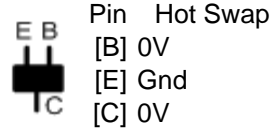
Q601 RS232 TX



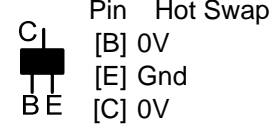
D628 RS232 RX Noise



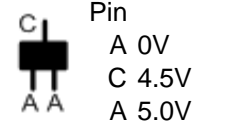
Q890/2 HDMI1/2



Q891 HDMI3/4

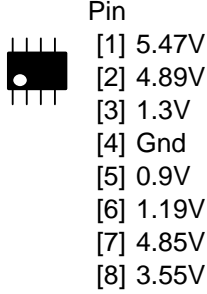


D633 RGB B+

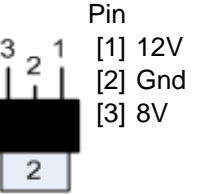


60PS60 MAIN (FRONT SIDE) SIMICONDUCTORS

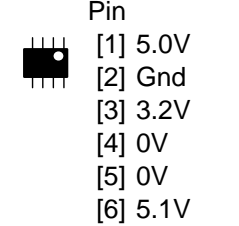
IC302 1.3V VDDC



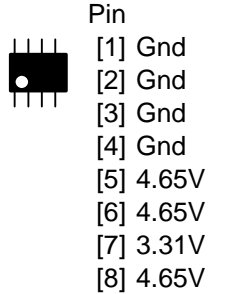
IC503 9V Reg



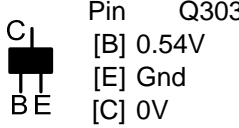
IC804 USB 5V



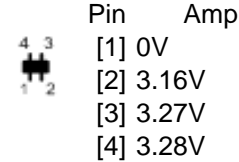
IC805 HDMI3 EDID



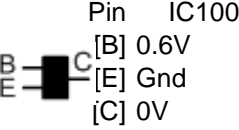
Q301 Turns on



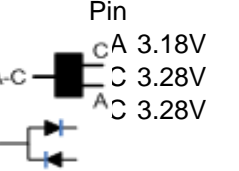
Q801 HDMI CEC



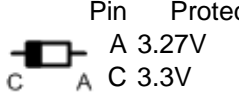
Q1002 Pow Down



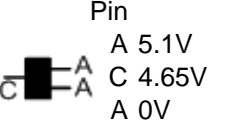
D303 Reset



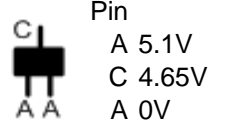
ZD601 Wired Remote T



D805 HDMI2 PWR



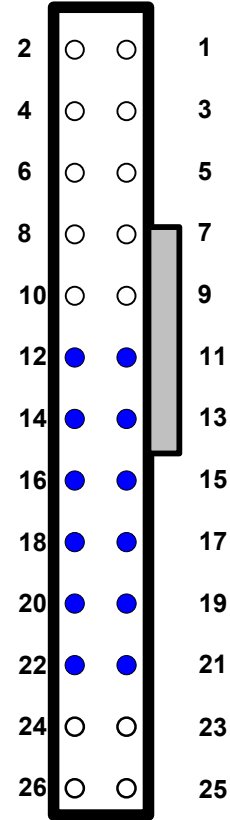
D806 HDMI3 PWR



60PS60 LVDS
P1003
WAVEFORMS

Connector P1003
Configuration

● indicates
signal pins.



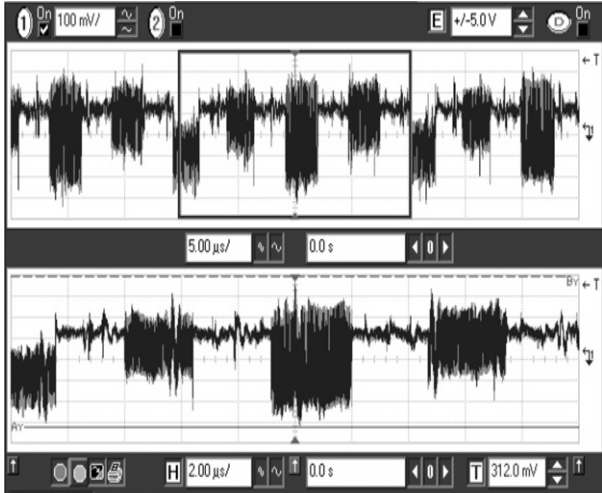
NOTE: LVDS P1003
Information

There are actually 12 pins carrying Video 2 pins are carrying clock signals (17 and 18) to the Control board. With high activity video, pins 21 and 22 would have signals present.

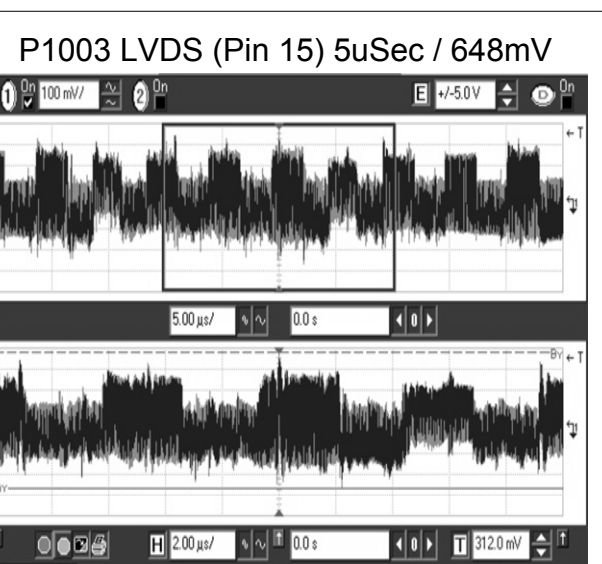
WAVEFORMS:

Waveforms taken using SMPTE Color Bar input. All readings give their Time Base related to scope settings. All waveforms taken from the P1003.

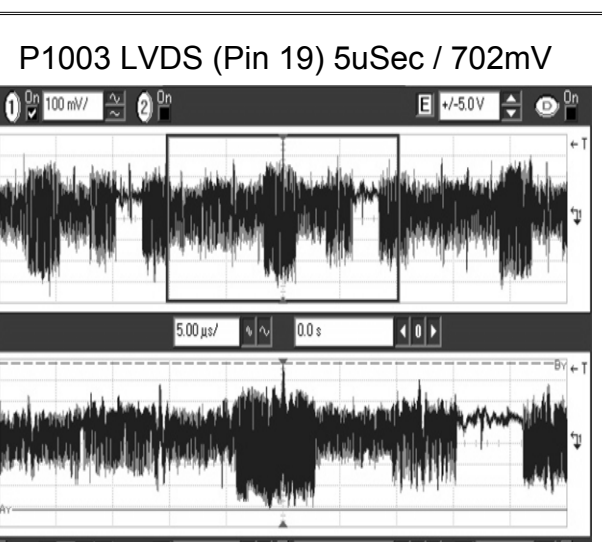
P1003 LVDS (Pin 11) 5uSec / 718mV



P1003 LVDS (Pin 11) 2uSec / 718mV

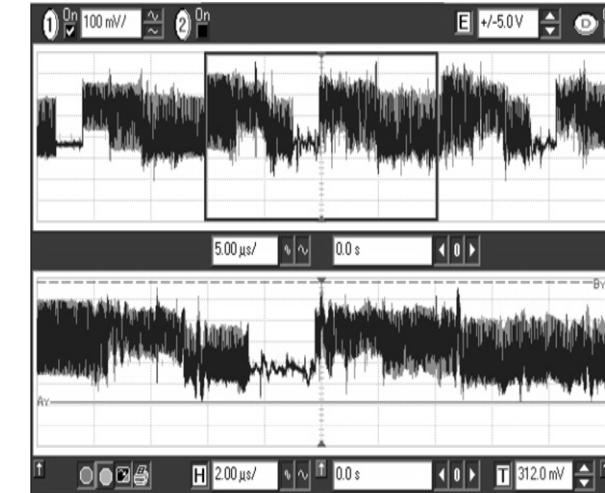


P1003 LVDS (Pin 15) 5uSec / 648mV

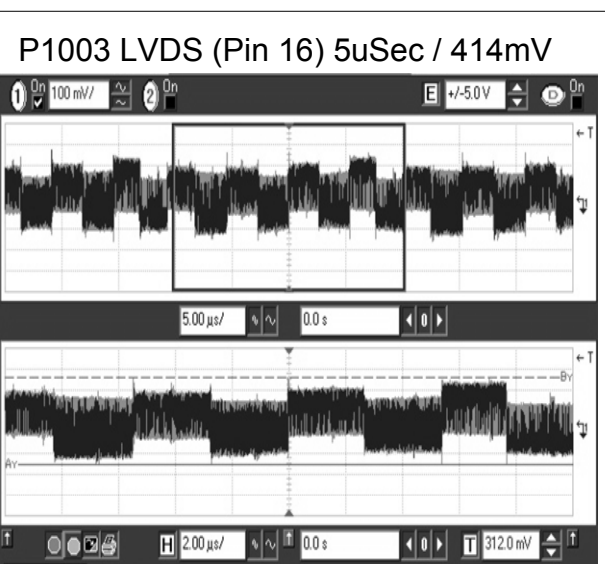


P1003 LVDS (Pin 15) 2uSec / 702mV

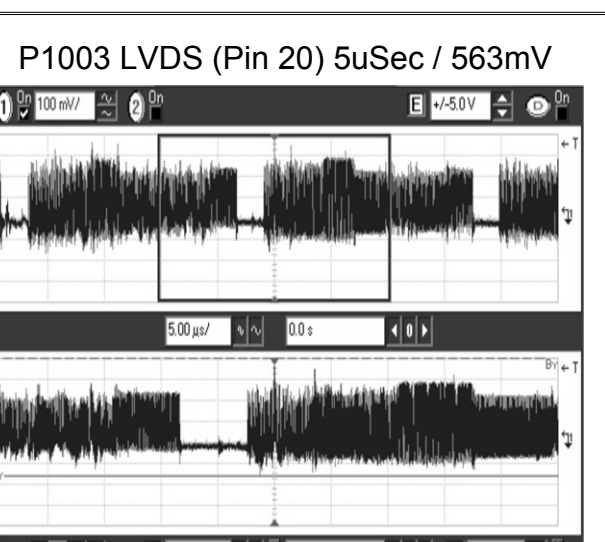
P1003 LVDS (Pin 12) 5uSec / 565mV



P1003 LVDS (Pin 12) 2uSec / 565mV

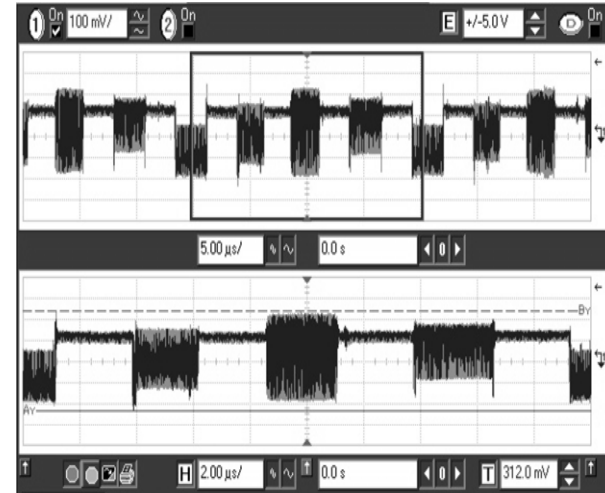


P1003 LVDS (Pin 16) 5uSec / 414mV

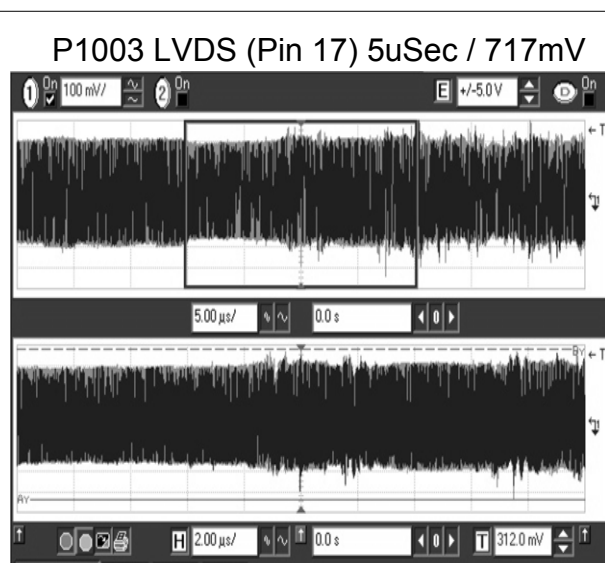


P1003 LVDS (Pin 16) 2uSec / 414mV

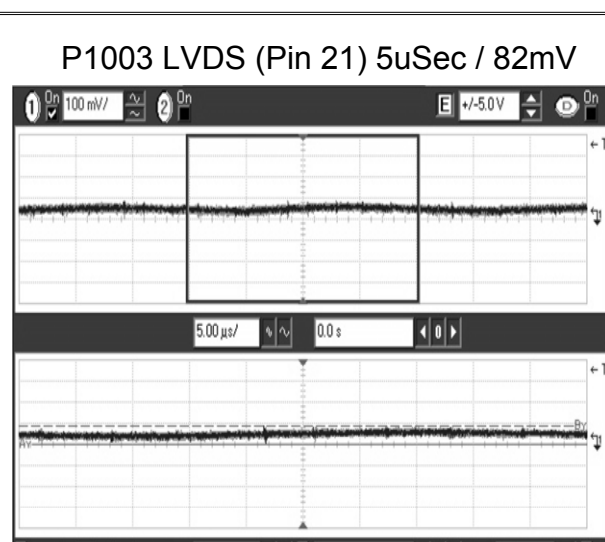
P1003 LVDS (Pin 13) 5uSec / 479mV



P1003 LVDS (Pin 13) 2uSec / 479mV

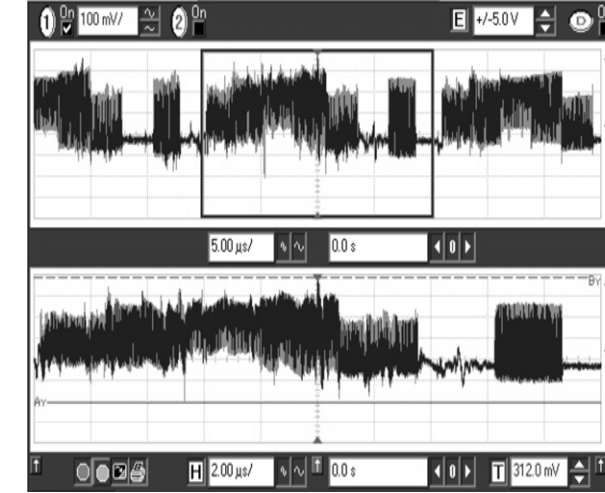


P1003 LVDS (Pin 17) 5uSec / 717mV

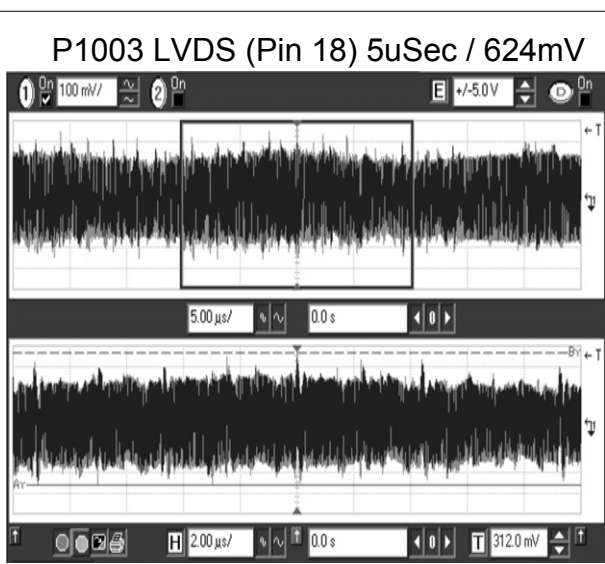


P1003 LVDS (Pin 17) 2uSec / 717mV

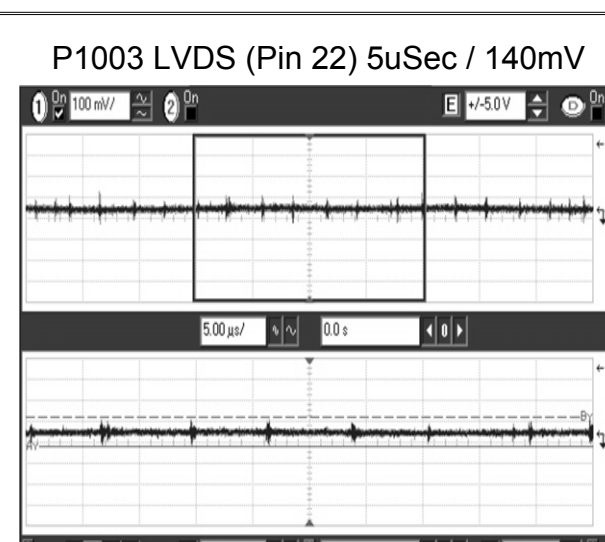
P1003 LVDS (Pin 14) 5uSec / 594mV



P1003 LVDS (Pin 14) 2uSec / 594mV

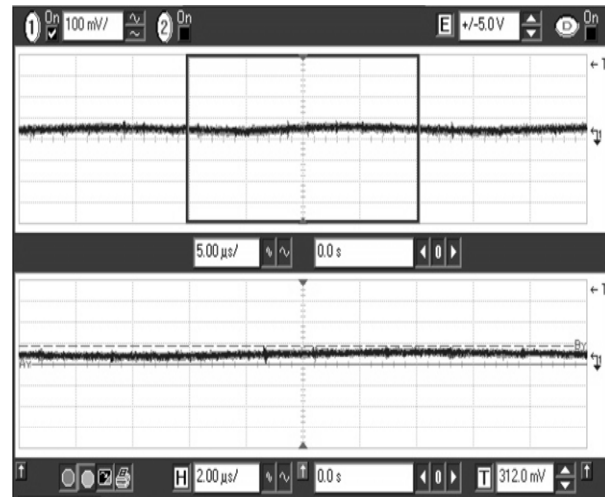


P1003 LVDS (Pin 18) 5uSec / 624mV



P1003 LVDS (Pin 18) 2uSec / 624mV

P1003 LVDS (Pin 21) 5uSec / 82mV



P1003 LVDS (Pin 21) 2uSec / 82mV

P1003 LVDS (Pin 22) 5uSec / 140mV



P1003 LVDS (Pin 22) 2uSec / 140mV

